

## Refine Search

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L20 not L14	8

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
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### Search History

DATE: Wednesday, October 10, 2007   
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<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>			
<u>L21</u>	L20 not l14	8	<u>L21</u>
<u>L20</u>	L17 and ((mapping or holding or handling) same (register))	22	<u>L20</u>
<u>L19</u>	L17 and ((mapping or holding or handling) same (emulat\$))	22	<u>L19</u>
<u>L18</u>	L17 and ((mapping or holding or handling) same (speculat\$))	1	<u>L18</u>
<u>L17</u>	L15 and speculat\$	22	<u>L17</u>
<u>L16</u>	L15 and specutlat\$	0	<u>L16</u>
<u>L15</u>	L6 and (subject same (code or processor or program))	22	<u>L15</u>
<u>L14</u>	L11 and (definition same register)	14	<u>L14</u>
<u>L13</u>	L11 and (machine same definition same register)	1	<u>L13</u>
<u>L12</u>	L11 and (machine same definition same architecture)	1	<u>L12</u>
<u>L11</u>	L10 and (definit\$ same (machine or architecture))	22	<u>L11</u>
<u>L10</u>	L8 and (pair\$4 same register\$)	22	<u>L10</u>
<u>L9</u>	L8 and pair\$4	22	<u>L9</u>

<u>L8</u>	L4 and (expect\$ same register)	22	<u>L8</u>
<u>L7</u>	l4 and unique	5	<u>L7</u>
<u>L6</u>	L5 and (block)	23	<u>L6</u>
<u>L5</u>	l4 and (memory same (machine or system or location) same (target or emulat\$))	23	<u>L5</u>
<u>L4</u>	L3 and(binary same translat\$)	23	<u>L4</u>
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<u>L2</u>	L1 and (exception\$ same handl\$)	406	<u>L2</u>
<u>L1</u>	register same (emulat\$ or subject) same (machine or architecture)	1958	<u>L1</u>

END OF SEARCH HISTORY